

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.-8. (Canceled)

9. (Previously Presented) A device having at least one liquid crystal panel, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity and being formed between the first substrate and the second substrate;

an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

10. (Original) A device according to claim 9, wherein said organic resin film comprises polyimide.

11. (Original) A device according to claim 9, wherein said pixel electrode is transparent.

12. (Original) A device according to claim 9, wherein said thin film transistor is a top-gate type in which said gate electrode is located above said channel region.

13. (Previously Presented) A device having at least one liquid crystal panel, said liquid crystal panel comprising:

- a first substrate having an insulating surface;

- a second substrate being opposed to the first substrate;

- at least one semiconductor element being formed over the first substrate, said semiconductor element including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

- wherein the channel region, the source and drain region of said one semiconductor element is formed in a semiconductor island;

- an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said semiconductor element;

- a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said semiconductor element through an opening formed in said organic resin film;

- a liquid crystal material having ferroelectricity and being formed between the first substrate and the second substrate, and

- an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

14. (Previously Presented) A device according to claim 13, wherein said organic resin film comprises polyimide.

15. (Previously Presented) A device according to claim 13, wherein said pixel electrode is transparent.

16. (Previously Presented) A device according to claim 13, wherein said semiconductor element is a top-gate type thin film transistor in which said gate electrode is located above said channel region.

17.-20. (Canceled)

21. (Previously Presented) A television comprising:
a tuner for receiving television radio wave;
a liquid crystal panel operationally connected to said tuner, said liquid crystal panel comprising:
a first substrate having an insulating surface;
a second substrate being opposed to the first substrate;
at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;
wherein the channel region, the source and drain region of said one thin film transistor is formed in a semiconductor island;
an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;
a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity and being formed between the first substrate and the second substrate;

an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

22. (Original) A television according to claim 21, wherein said organic resin film comprises polyimide.

23. (Original) A television according to claim 21, wherein said pixel electrode is transparent.

24. (Original) A television according to claim 21, wherein said thin film transistor is a top-gate type in which said gate electrode is located above said channel region.

25.-32. (Canceled)

33. (Previously Presented) A portable computer having a liquid crystal panel, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity and being formed between the first substrate and the second substrate;

an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

34. (Original) A portable computer according to claim 33, wherein said organic resin film comprises polyimide.

35. (Original) A portable computer according to claim 33, wherein said pixel electrode is transparent.

36. (Original) A portable computer according to claim 33, wherein said thin film transistor is a top-gate type in which said gate electrode is located above said channel region.

37.-49. (Canceled)

50. (Previously Presented) A device according to claim 9, wherein the semiconductor island is a crystalline semiconductor island.

51. (Previously Presented) A device according to claim 13, wherein the semiconductor island is a crystalline semiconductor island.

52. (Previously Presented) A television according to claim 21, wherein the semiconductor island is a crystalline semiconductor island.

53. (Canceled)

54. (Previously Presented) A portable computer according to claim 33, wherein the semiconductor island is a crystalline semiconductor island.

55.-56. (Canceled)

57. (Previously Presented) A device having at least one liquid crystal panel, said liquid crystal panel comprising:

- a first substrate having an insulating surface;

- a second substrate opposed to said first substrate;

- at least one thin film transistor formed over said first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

- wherein said channel region, said source and drain region of said one thin film transistor is formed in a semiconductor island;

- a leveling film formed over said first substrate to provide a leveled upper surface over said first substrate, said leveling film covering said thin film transistor;

- a pixel electrode formed on said leveled upper surface, said pixel electrode electrically connected to said thin film transistor through an opening formed in said leveling film;

- a liquid crystal material having ferroelectricity and being formed between said first substrate and said second substrate;

- an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

58. (Previously Presented) A television comprising:

- a tuner for receiving television radio wave;

a liquid crystal panel operationally connected to said tuner, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate opposed to said first substrate;

at least one thin film transistor being formed over said first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein said channel region, said source and drain regions of said one thin film transistor is formed in a semiconductor island;

a leveling film formed over said first substrate to provide a leveled upper surface over said first substrate, said leveling film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode electrically connected to said thin film transistor through an opening formed in said leveling film;

a liquid crystal material having ferroelectricity and being formed between said first substrate and said second substrate;

an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

59. (Previously Presented) A portable computer having a liquid crystal panel, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate opposed to said first substrate;

at least one thin film transistor being formed over said first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein said channel region, said source and drain regions of said one thin film transistor is formed in a semiconductor island;

a leveling film formed over said first substrate to provide a leveled upper surface over said first substrate, said leveling film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said leveling film;

a liquid crystal material having ferroelectricity and being formed between said first substrate and said second substrate;

an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

60. (Previously Presented) A device according to claim 57, wherein said semiconductor island is a crystalline semiconductor island.

61. (Previously Presented) A device according to claim 57, wherein said pixel electrode is transparent.

62. (Previously Presented) A television according to claim 58, wherein said semiconductor island is a crystalline semiconductor island.

63. (Previously Presented) A television according to claim 58, wherein said pixel electrode is transparent.

64. (Previously Presented) A portable computer according to claim 59, wherein said semiconductor island is a crystalline semiconductor island.

65. (Previously Presented) A portable computer according to claim 59, wherein said pixel electrode is transparent.

66. (Previously Presented) A projector having at least one liquid crystal panel, said liquid crystal panel comprising:

- a first substrate having an insulating surface;

- a second substrate opposed to said first substrate;

- at least one thin film transistor being formed over said first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

- wherein said channel region, said source and drain regions of said one thin film transistor is formed in a semiconductor island;

- a leveling film formed over said first substrate to provide a leveled upper surface over said first substrate, said leveling film covering said thin film transistor;

- a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said leveling film;

- a liquid crystal material having ferroelectricity and being formed between said first substrate and said second substrate;

- an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

67. (Previously Presented) A projector according to claim 66, wherein said semiconductor island is a crystalline semiconductor island.

68. (Previously Presented) A projector according to claim 66, wherein said pixel electrode is transparent.

69. (Previously Presented) A device having at least one liquid crystal panel, said liquid crystal panel comprising:

- a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having anti-ferroelectricity and being formed between the first substrate and the second substrate;

an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

70. (Previously Presented) A device according to claim 69, wherein said organic resin film comprises polyimide.

71. (Previously Presented) A device according to claim 69, wherein said pixel electrode is transparent.

72. (Previously Presented) A device according to claim 69, wherein said thin film transistor is a top-gate type in which said gate electrode is located above said channel region.

73. (Previously Presented) A television comprising:
a tuner for receiving television radio wave;

a liquid crystal panel operationally connected to said tuner, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having anti-ferroelectricity and being formed between the first substrate and the second substrate;

an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

74. (Previously Presented) A television according to claim 73, wherein said organic resin film comprises polyimide.

75. (Previously Presented) A television according to claim 73, wherein said pixel electrode is transparent.

76. (Previously Presented) A television according to claim 73, wherein said thin film transistor is a top-gate type in which said gate electrode is located above said channel region.

77. (Previously Presented) A portable computer having a liquid crystal panel, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having anti-ferroelectricity and being formed between the first substrate and the second substrate;

an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

78. (Previously Presented) A portable computer according to claim 77, wherein said organic resin film comprises polyimide.

79. (Previously Presented) A portable computer according to claim 77, wherein said pixel electrode is transparent.

80. (Previously Presented) A portable computer according to claim 77, wherein said thin film transistor is a top-gate type in which said gate electrode is located above said channel region.

81. (Previously Presented) A device having at least one liquid crystal panel, said liquid crystal panel comprising:

- a first substrate having an insulating surface;

- a second substrate opposed to said first substrate;

- at least one thin film transistor formed over said first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

- wherein said channel region, said source and drain region of said one thin film transistor is formed in a semiconductor island;

- a leveling film formed over said first substrate to provide a leveled upper surface over said first substrate, said leveling film covering said thin film transistor;

- a pixel electrode formed on said leveled upper surface, said pixel electrode electrically connected to said thin film transistor through an opening formed in said leveling film;

- a liquid crystal material having anti-ferroelectricity and being formed between said first substrate and said second substrate;

- an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

82. (Previously Presented) A device according to claim 81, wherein said pixel electrode is transparent.

83. (Previously Presented) A device according to claim 81, wherein said thin film transistor is a top-gate type in which said gate electrode is located above said channel region.

84. (Previously Presented) A device according to claim 81, wherein said semiconductor island is a crystalline semiconductor island.

85. (Previously Presented) A television comprising:
a tuner for receiving television radio wave;
a liquid crystal panel operationally connected to said tuner, said liquid crystal panel comprising:
a first substrate having an insulating surface;
a second substrate opposed to said first substrate;
at least one thin film transistor being formed over said first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;
wherein said channel region, said source and drain regions of said one thin film transistor is formed in a semiconductor island;
a leveling film formed over said first substrate to provide a leveled upper surface over said first substrate, said leveling film covering said thin film transistor;
a pixel electrode formed on said leveled upper surface, said pixel electrode electrically connected to said thin film transistor through an opening formed in said leveling film;
a liquid crystal material having anti-ferroelectricity and being formed between said first substrate and said second substrate;
an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

86. (Currently Amended) A television according to claim ~~[[81]]~~ 85, wherein said pixel electrode is transparent.

87. (Currently Amended) A television according to claim ~~[[81]]~~ 85, wherein said thin film transistor is a top-gate type in which said gate electrode is located above said channel region.

88. (Currently Amended) A television according to claim ~~[[81]]~~ 85, wherein said semiconductor island is a crystalline semiconductor island.

89. (Previously Presented) A portable computer having a liquid crystal panel, said liquid crystal panel comprising:

- a first substrate having an insulating surface;

- a second substrate opposed to said first substrate;

- at least one thin film transistor being formed over said first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

- wherein said channel region, said source and drain regions of said one thin film transistor is formed in a semiconductor island;

- a leveling film formed over said first substrate to provide a leveled upper surface over said first substrate, said leveling film covering said thin film transistor;

- a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said leveling film;

- a liquid crystal material having anti-ferroelectricity and being formed between said first substrate and said second substrate;

- an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

90. (Previously Presented) A portable computer according to claim 89, wherein said pixel electrode is transparent.

91. (Previously Presented) A portable computer according to claim 89, wherein said thin film transistor is a top-gate type in which said gate electrode is located above said channel region.

92. (Previously Presented) A portable computer according to claim 89, wherein said semiconductor island is a crystalline semiconductor island.

93. (Previously Presented) A projector having at least one liquid crystal panel, said liquid crystal panel comprising:

- a first substrate having an insulating surface;

- a second substrate opposed to said first substrate;

- at least one thin film transistor being formed over said first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

- wherein said channel region, said source and drain regions of said one thin film transistor is formed in a semiconductor island;

- a leveling film formed over said first substrate to provide a leveled upper surface over said first substrate, said leveling film covering said thin film transistor;

- a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said leveling film;

- a liquid crystal material having anti-ferroelectricity and being formed between said first substrate and said second substrate;

an opposed electrode formed over said second substrate and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

94. (Previously Presented) A projector according to claim 93, wherein said semiconductor island is a crystalline semiconductor island.

95. (Previously Presented) A projector according to claim 93, wherein said pixel electrode is transparent.

96. (Previously Presented) A projector according to claim 66, wherein said leveling film comprises organic resin.

97. (Previously Presented) A projector according to claim 93, wherein said leveling film comprises organic resin.